IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani et al Art Unit : 1765

Examiner : Robert Kunemund Serial No.: 08/690,747

: August 1, 1996

: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE Title

Commissioner for Patents Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTC-1449, copies of which are enclosed.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Motice of Allowance. A check for \$240 in payment of the late submission fee of \$1.17(p) is enclosed.

Applicants call attention to the attached Information Disclosure Statement and document listed on form PTO-1449.

This IDS is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice to that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicants to comply fully.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby destify under 37 CFR \$1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and . addressed to the Commissioner for Patents, Washington, D.C. 20231.

Date of Signature

Sandy Malec

of Person Signing or Printed Name Typed Certificate

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Consideration of the foregoing and enclosures plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 7// ///0

Scott C. Harris

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